

GUARANTEED BOOTSTRAP HOLD-UP CIRCUIT FOR BUCK HIGH SIDE SWITCH

FIELD OF THE INVENTION

The present invention is generally related to switching power supplies, and
5 more particularly to buck regulator switching power supplies including those
having a bootstrap capacitor.

BACKGROUND OF THE INVENTION

Buck regulator switching power supplies typically include a bootstrap
10 capacitor used to boost a voltage at a gate of a high side MOSFET. The purpose
of the bootstrap capacitor is to allow the high side MOSFET to be enhanced “on”
since its gate voltage must be greater than the voltage normally available in the
application.

In certain operating conditions, the bootstrap capacitor of the buck
15 regulator switching power supply can lose its charge. When this happens, the
power stage does not function properly, and in some circumstances, the power
stage can latch “off”.

There is desired an improved buck regulator switching power supply that
ensures the bootstrap capacitor is properly recharged, particularly when the buck
20 regulator switching power supply has a high duty cycle to provide large output
voltages, including outputs very close to the input voltage.

SUMMARY OF THE INVENTION

The present invention achieves technical advantages as a buck regulator switching power supply ensuring the bootstrap capacitor is properly recharged, particularly during high duty cycles. One embodiment of the present invention includes a detector and controller sensing the voltage across the bootstrap capacitor, and selectively enabling a low side device to improve the charging of the bootstrap capacitor. In one embodiment, a comparator senses the voltage across the bootstrap capacitor, in reference to a voltage reference. A controller circuit including the comparator responsively enables a low side device to facilitate the recharging of the bootstrap capacitor when it is determined the voltage across the bootstrap capacitor is dropping or is becoming insufficient for suitable operation. The low side device may comprise a low side FET, whereby the source of this low side FET is connected to ground and selectively provides ground to the non-charging side of the bootstrap capacitor to improve the charging thereof, such as during high duty cycle conditions.

The present invention also includes a switch for selectively coupling the input voltage to the bootstrap capacitor during recharging thereof. The comparator circuit is also responsive to the phase output of the circuit, whereby a controller provides a gate drive signal to the high side FET as a function of the comparator output. The low side device is enabled anti-phase with the high side FET.

Advantageously, the present invention provides an input to output ratio of beyond 99%, dependent on charge leakage of the boot strap capacitor and highside driver. This is achieved because the low side device is not enabled every cycle and selectively recharges the bootstrap capacitor to ensure effective

boosting at a high side FET.

BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 is a schematic diagram of the present invention including a controller responsive to a comparator sensing the bootstrap capacitor voltage, and a low side FET selectively enabled by the controller during only some cycles to
5 maintain a voltage at the bootstrap capacitor; and

Figure 2 is a waveform diagram illustrating the signals at various nodes of the circuit to achieve a high input to output voltage ratio.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

Referring now to Figure 1, there is generally shown at 10 a buck regulator switching power supply according to the present invention. Power supply 10 is seen to include a gate driver circuit 12 selectively driving the gate of a high side FET transistor Q1. A controller circuit 14 is seen to control gate driver 12, as well as selectively driving the gate of a low side device, seen to comprise a low side FET Q2. Circuit 14 is seen to comprise a controller 16 generating the two output control signals, and responsively receiving an output signal from comparator 18.

According to the present invention, comparator 18 is coupled to and senses the voltage across the bootstrap capacitor, identified as C_{boot} . Comparator 18 compares this voltage across the bootstrap capacitor to a voltage reference provided thereto, and provides an output signal on line 20 which is indicative of whether the voltage of the C_{boot} capacitor has fallen below the voltage reference provided to comparator 18. If so, controller 16 responsively responds to the output signal provided on line 20 to selectively enable the low side FET Q2 during the anti-phase of circuit 10, thereby facilitating the recharging of bootstrap capacitor C_{boot} .

When the duty cycle of power supply 10 is large, such that the output voltage at the circuit 10 output is desired to be substantially close to the voltage at the input of circuit 10, controller circuit 14 ensures the effectiveness of bootstrap capacitor C_{boot} through the selective enabling of low side FET Q2. By selectively providing a ground to the non-charging side of bootstrap capacitor C_{boot} during the anti-phase of circuit 10, the C_{boot} capacitor is effectively recharged upon the closure of switch SW1 and an effective boost voltage is provided to high side FET

Q1, particularly, during this high duty cycle.

Referring to Figure 2, there is shown a timing diagram of the various signals at various nodes of circuit 10. It can be appreciated in Figure 2 that the gate driver 12 effectively provides an effective boost voltage to bootstrap capacitor C_{boot} even during a high duty cycle. The present invention achieves technical advantages by both driving the high side FET Q1 as a function of the sensed voltage of the bootstrap capacitor C_{boot} , and also by only enabling the low side device Q2 when needed to recharge bootstrap capacitor C_{boot} to maintain its effectiveness, particularly during high duty cycle operation. As compared to prior art buck regulator switching power supplies which may only achieve 92% efficiency, the present invention achieves approximately 99% efficiency by not enabling the low side FET Q2 every cycle. For instance, the low side FET Q2 may be enabled only once every 50 cycles. As also can be appreciated in Figure 2, the comparator 18 provides an output control signal on line 20 as a function of the sensed charged voltage of bootstrap capacitor C_{boot} , and the sensed voltage at the phased output as a function of the voltage provided to the voltage reference.

Referring back to Figure 1, if the output of controller 16 on line 22 is high, the gate of the high side FET Q1 is connected to the charged side of bootstrap capacitor C_{boot} via the gate driver circuit 12. This provides the bootstrap effect, and the gate of high side FET Q1 remains connected to capacitor C_{boot} when switch SW1 is on to refresh the C_{boot} capacitor, and thus which becomes the input voltage V_{in} .

When the output of controller 16 provides a low signal on control line 22, the gate driver circuit 12 pulls the gate of high side FET Q1 to the phased output node PH, which responsively turns off the high side FET Q1 and the phase node

PH goes to low.

Low side FET Q2 is only switched on during the anti-phase of high side FET Q1. When low side FET Q2 is enabled via control line 24, the ground is connected to the non-charging side of bootstrap capacitor C_{boot} and controller 16
5 closes switch SW1 via control line 26. This arrangement effectively provides a voltage differential of V_{in} across bootstrap capacitor C_{boot} such that the bootstrap capacitor is effective, even during duty cycles approaching 99%. Advantageously, low side FET Q2 is only selectively turned on during high duty cycles, and may be on only once every 50 cycles.

10 The present invention is also particularly useful in low voltage applications when the output voltage needs to be substantially close to the input voltage, and provides a cost effective solution.

Though the invention has been described with respect to a specific preferred embodiment, many variations and modifications will become apparent
15 to those skilled in the art upon reading the present application. It is therefore the intention that the appended claims be interpreted as broadly as possible in view of the prior art to include all such variations and modifications. For instance, low side FET Q2 could be substituted with a bipolar device, or another suitable switching component to achieve the same function of ensuring the effectiveness
20 of the bootstrap capacitor C_{boot} .